



1745
\$

Docket: A-70143/RMS/AXG/JML

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor:	Cynthia Briscoe	
Application No.:	09/466,325	
Filing Date:	December 17, 1999	Examiner: Brian J. Sines
Title:	Microchip Device and Array	Group Art Unit: 1743

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97(C)

Commissioner for Patents
Washington, D.C. 20231

I hereby certify that this document is being sent via First Class U.S.
mail addressed to: Commissioner for Patents, Washington, D.C.
20231 on 9 this day of September 2002.

Christine P. Peters

Christine P. Peters

Dear Sir:

Pursuant to 37 CFR 1.97(c), the references listed on the attached Form PTO-1449 (8 sheets) are brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. Copies of the identified references are enclosed as necessary. This Information Disclosure Statement ("IDS") is filed on or before the mailing date of a final office action or a notice of allowance is accompanied by the fee set forth in 37 CFR 1.17(p).

Payment of Fee Under 37 CFR 1.17(p)

The fee under 37 CFR 1.17(p), required for submission of an IDS under 37 CFR 1.97(c), is enclosed.

Respectfully submitted,

Date: 9/4/02

DORSEY & WHITNEY LLP

By: Robin M. Silva

Robin M. Silva (Reg. No. 38,304)
Filed under 37 C.F.R. §1.34(a)
Suite 3400
Four Embarcadero Center
San Francisco, CA 94111-4180
(415) 781-1989

RECEIVED
SEP 10 2002
MAIL ROOM

09/10/2002 AOSMAN1 00000012 09466325

01 FC:126

180.00 OP

1091394